TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOS VI)

2SK4107

Switching Regulator Applications

Unit: mm

• Low drain–source ON resistance : RDS (ON) = 0. 33 Ω (typ.)

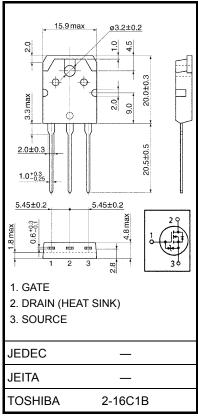
• High forward transfer admittance : $|Y_{fs}| = 8.5 \mathrm{\ S}$ (typ.)

• Low leakage current : $I_{DSS} = 100 \mu A (max) (V_{DS} = 500 V)$

• Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	500	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	ID	15	Α	
	Pulse (Note 1)	I _{DP}	60	Α	
Drain power dissipation	n (Tc = 25°C)	PD	150	W	
Single-pulse avalanche energy (Note 2)		E _{AS}	765	mJ	
Avalanche current		I _{AR}	15	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 5.78 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



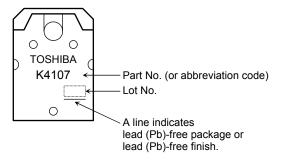
Electrical Characteristics (Ta = 25°C)

Charac	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V		_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 7.0 A	-	0.33	0.4	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 7.0 A	4.0	8.5	_	S
Input capacitano	e	C _{iss}			2450	_	pF
Reverse transfe	r capacitance	citance C_{rss} $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$			15	_	
Output capacitar	utput capacitance C _{oss}		_	220	_	1	
Switching time	Rise time	t _r	V_{GS} $0V$ $R_{L}=30\Omega$ $V_{DD}=210V$	_	50	_	ns ns
	Turn-on time	t _{on}		_	90	_	
	Fall time	t _f		_	45	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$		175		
Total gate charg plus gate-drain)		Qg			48	_	
		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	26	_	nC
		Q _{gd}			22		

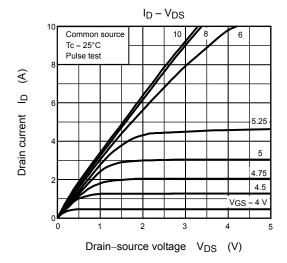
Source-Drain Ratings and Characteristics (Ta = 25°C)

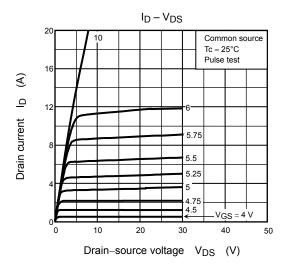
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	15	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	_	60	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V	_	1050	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs	_	13	_	μC

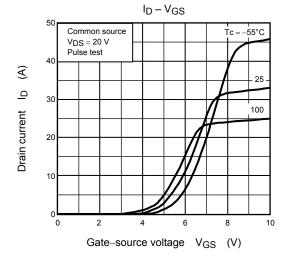
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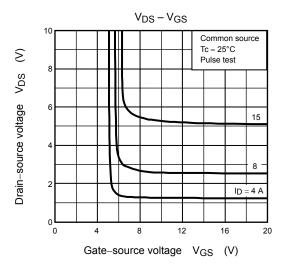


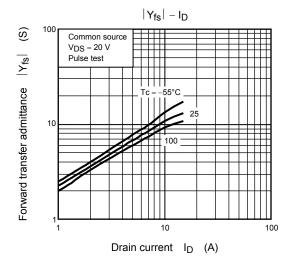
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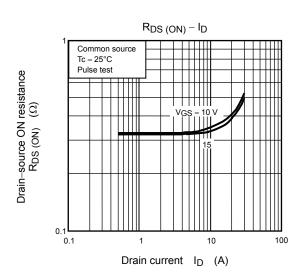




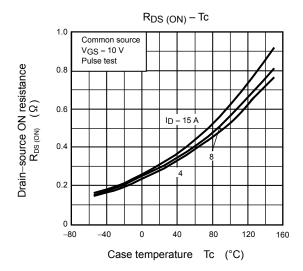


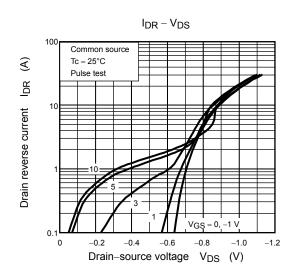


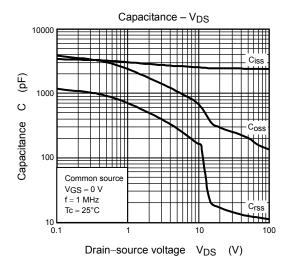


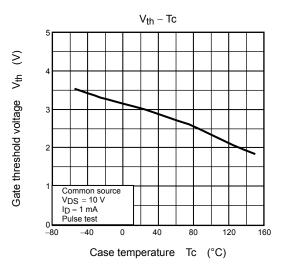


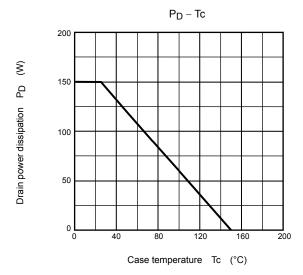
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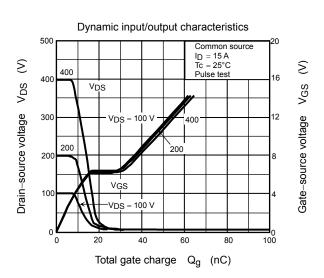


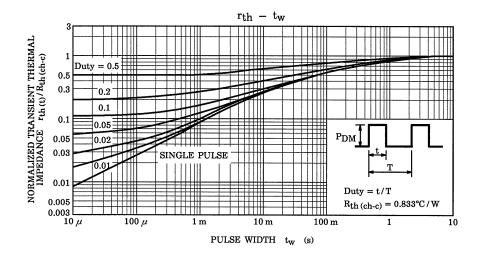




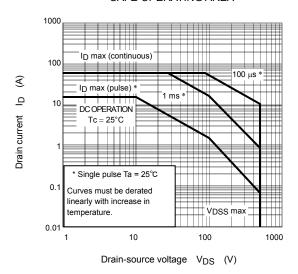


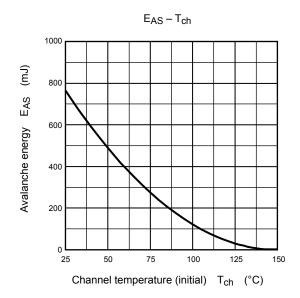


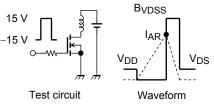












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 5.78~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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